

L Number	Hits	Search Text	DB	Time stamp
1	277	map\$3 adj3 technology	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 14:07
2	192	portion and (map\$3 adj3 technology)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 14:08
3	109	optimiz\$5 and (portion and (map\$3 adj3 technology))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 14:08

L Number	Hits	Search Text	DB	Time stamp
1	68625	map\$4 same (method or technolog\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:54
2	51	(map\$4 same (method or technolog\$4)) and (divide adj block)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:08
3	3	((map\$4 same (method or technolog\$4)) and (divide adj block)) and replacement	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:56
4	9975	(map\$4 same (method or technolog\$4)) and replacement	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:18
5	7730	((map\$4 same (method or technolog\$4)) and replacement) and block	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:09
6	72	((map\$4 same (method or technolog\$4)) and replacement) and (sub-network)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:10
7	85	((map\$4 same (method or technolog\$4)) and replacement) and block) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:15
8	265	(map\$4 same (method or technolog\$4)) and (replacement near5 block)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:53
9	7378	map\$4 adj3 (method or technolog\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:54
10	905	(map\$4 adj3 (method or technolog\$4)) and replacement	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:56
11	326	((map\$4 adj3 (method or technolog\$4)) and replacement) and candidate	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:57
12	277	((map\$4 adj3 (method or technolog\$4)) and replacement) and candidate) and (block or sub-network)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:58
13	275	((map\$4 adj3 (method or technolog\$4)) and replacement) and candidate) and (block or sub-network)) and select\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 10:30

14	3	(((((map\$4 adj3 (method or technolog\$4)) and replacement) and candidate) and (block or sub-network)) and select\$4) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/12 10:01
15	19	((map\$4 adj3 (method or technolog\$4)) and replacement) and 716/\$.ccls.	USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/12 10:01
16	81	(((((map\$4 adj3 (method or technolog\$4)) and replacement) and candidate) and (block or sub-network)) and select\$4) and optimization	USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/12 10:31

L Number	Hits	Search Text	DB	Time stamp
1	68625	map\$4 same (method or technolog\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:07
2	51	(map\$4 same (method or technolog\$4)) and (divide adj block)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:08
3	3	((map\$4 same (method or technolog\$4)) and (divide adj block)) and replacement	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:11
4	9975	(map\$4 same (method or technolog\$4)) and replacement	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:18
5	7730	((map\$4 same (method or technolog\$4)) and replacement) and block	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:09
6	72	((map\$4 same (method or technolog\$4)) and replacement) and (sub-network)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:10
7	85	((map\$4 same (method or technolog\$4)) and replacement) and block) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:15
8	265	(map\$4 same (method or technolog\$4)) and (replacement near5 block)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/12 09:19

Optimization
 replacement
 command
 sub-
 com-
 Select

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030108872 A1	20030612	81	Genomics-assisted rapid identification of targets	435/6
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030105721 A1	20030605	329	Systems and methods for secure transaction management and electronic rights protection	705/54
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030104602 A1	20030605	52	Adipocyte complement related protein zacr3X2	435/226
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030096247 A1	20030522	305	Human cDNAs and proteins and uses thereof	435/6
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030093229 A1	20030515	28	System and method for improved computer drug design	702/27
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030092620 A1	20030515	58	Use of adipsin/complement factor D in the treatment of metabolic related disorders	514/12
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030092011 A1	20030515	305	Human cDNAs and proteins and uses thereof	435/6
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030088784 A1	20030508	330	Systems and methods for secure transaction management and electronic rights protection	713/189
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030082537 A1	20030501	96	Methods for genetic analysis of DNA to detect sequence variances	435/6
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030073101 A1	20030417	92	Restriction enzyme genotyping	435/6
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030068831 A1	20030410	47	Proteins and druggable regions of proteins	436/518

	U	1	Document ID	Issue Date	Pages	Title	Curr nt OR
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030068651 A1	20030410	49	Multi-target analysis of gene families for chemistry of high affinity and selective small molecules and other therapeutics	435/7.1
13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030068650 A1	20030410	48	Target analysis for chemistry of specific and broad spectrum anti-infectives and other therapeutics	435/7.1
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030064380 A1	20030403	106	Anergy-regulated molecules	435/6
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030027248 A1	20030206	305	Human cDNAs and proteins and uses thereof	435/69.1
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030027161 A1	20030206	306	Human cDNAs and proteins and uses thereof	435/6
17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020188108 A1	20021212	472	X-ray crystal structures of functional ribosome complexes containing transfer RNA and model messenger RNAs and methods of use	530/388.4
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020182692 A1	20021205	31	ISOLATED HUMAN UDP-GLYCOSYLTRANSFERASE PROTEINS	435/183
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020173005 A1	20021121	64	Methods and materials relating to novel CD39-like polypeptides	435/69.1
20.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020157063 A1	20021024	1889	Implicit mapping of technology independent network to library cells	716/1

	U	1	Document ID	Issue Date	Pages	Title	Current OR
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020155581 A1	20021024	199	Use of recombinant parainfluenza viruses (PIVs) as vectors to protect against infection and disease caused by PIV and other human pathogens	435/235.1
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020146772 A1	20021010	45	Methods and materials relating to novel CD39-like polypeptides	435/69.1
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020141652 A1	20021003	65	METHODS AND DEVICES FOR PROCESSING DATA AND NOTABLY FOR COMPRESSING AND DECOMPRESSING IMAGES	382/248
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020137677 A1	20020926	107	Soluble zalphall cytokine receptors	514/12
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020127670 A1	20020912	43	Riboflavin synthase genes and enzymes and methods of use	435/183
26	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020127559 A1	20020912	75	Pharmacogenomics and identification of drug targets by reconstruction of signal transduction pathways based on sequences of accessible regions	435/6
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020112171 A1	20020815	330	Systems and methods for secure transaction management and electronic rights protection	713/185
28	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020102604 A1	20020801	262	Full-length human cDNAs encoding potentially secreted proteins	435/7.1
29	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020081603 A1	20020627	74	Databases of regulatory sequences; methods of making and using same	435/6
30	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020076711 A1	20020620	70	Methods for designing exogenous regulatory molecules	435/6

	U	1	Document ID	Issue Date	Pages	Title	Curr nt OR
31	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020058018 A1	20020516	58	Novel interleukin-3 and uses thereof	424/85.2
32	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020052023 A1	20020502	47	Lumazine and riboflavin synthase	435/69.1
33	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020048369 A1	20020425	355	Systems and methods for secure transaction management and electronic rights protection	380/277
34	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020042059 A1	20020411	103	Compositions and methods for analysis of nucleic acids	435/6
35	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020034757 A1	20020321	140	Single-molecule selection methods and compositions therefrom	435/6
36	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20010044431 A1	20011122	41	Prevention of ovarian cancer by administration of products that induce biologic effects in the ovarian epithelium	514/179
37	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6511808 B2	20030128	72	Methods for designing exogenous regulatory molecules	435/6
38	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6498866 B2	20021224	59	Methods and devices for processing data and notably for compressing and decompressing images	382/248
39	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6480791 B1	20021112	56	Parallel methods for genomic analysis	702/20
40	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6476211 B1	20021105	88	Methods and materials relating to CD39-like polypeptides	536/23.5
41	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6475736 B1	20021105	81	Methods for genetic analysis of DNA using biased amplification of polymorphic sites	435/6
42	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6473746 B1	20021029	33	Method of verifying pretrained neural net mapping for use in safety-critical software	706/15

	U	1	Document ID	Issue Date	Pages	Titl	Current OR
43	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6472173 B1	20021029	55	Chemokine receptor obtained from a cDNA library of fetal liver-spleen	435/69.1
44	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6447771 B1	20020910	65	Methods and materials relating to novel CD39-like polypeptides	424/94.61
45	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6427140 B1	20020730	316	Systems and methods for secure transaction management and electronic rights protection	705/80
46	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6403320 B1	20020611	99	Support bound probes and methods of analysis using the same	435/6
47	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6389402 B1	20020514	315	Systems and methods for secure transaction management and electronic rights protection	705/51
48	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6387645 B1	20020514	46	Methods and materials relating to novel CD39-like polypeptides	435/21
49	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6383789 B1	20020507	29	Isolated human UDP-glycosyltransferase , nucleic acid molecules encoding human UDP-glycosyltransferase , and uses thereof	435/193
50	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6363488 B1	20020326	315	Systems and methods for secure transaction management and electronic rights protection	713/201
51	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6356700 B1	20020312	NA	Efficient light engine systems, components and methods of manufacture	385/147
52	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6350597 B1	20020226	NA	Riboflavin synthase genes and enzymes and methods of use	435/183
53	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6335013 B1	20020101	NA	Methods and materials relating to CD39-like polypeptides	424/94.61
54	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6323013 B1	20011127	NA	Lumazine synthase and riboflavin synthase	435/183

	U	1	Document ID	Issue Date	Pages	Title	Current OR
55	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6287765 B1	20010911	131	Methods for detecting and identifying single molecules	435/6
56	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6262976 B1	20010717	NA	System and method for network flow optimization using traffic classes	370/254
57	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6253193 B1	20010626	NA	Systems and methods for the secure transaction management and electronic rights protection	705/57
58	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6251620 B1	20010626	NA	Three dimensional structure of a ZAP tyrosine protein kinase fragment and modeling methods	435/15
59	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6237786 B1	20010529	NA	Systems and methods for secure transaction management and electronic rights protection	213/153
60	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6197557 B1	20010306	NA	Compositions and methods for analysis of nucleic acids	435/91.2
61	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6161130 A	20001212	NA	Technique which utilizes a probabilistic classifier to detect "junk" e-mail by automatically updating a training and re-training the classifier based on the updated training set	709/206
62	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6146866 A	20001114	NA	Lumazine synthase and riboflavin synthase	435/193
63	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6111582 A	20000829	143	System and method of image generation and encoding using primitive reprojection	345/421
64	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5982891 A	19991109	NA	Systems and methods for secure transaction management and electronic rights protection	705/54

	U	1	Document ID	Issue Date	Pages	Title	Current OR
65	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5949876 A	19990907	NA	Systems and methods for secure transaction management and electronic rights protection	705/80
66	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5937557 A	19990817	NA	Fingerprint-acquisition apparatus for access control; personal weapon and other systems controlled thereby	42/70.01
67	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5917912 A	19990629	NA	System and methods for secure transaction management and electronic rights protection	713/187
68	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5915019 A	19990622	NA	Systems and methods for secure transaction management and electronic rights protection	705/54
69	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5910987 A	19990608	311	Systems and methods for secure transaction management and electronic rights protection	705/52
70	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5892900 A	19990406	NA	Systems and methods for secure transaction management and electronic rights protection	713/200
71	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5853989 A	19981229	NA	Method of characterisation of genomic DNA	435/6
72	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5842194 A	19981124	NA	Method of recognizing images of faces or general images using fuzzy combination of multiple resolutions	706/52
73	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5830662 A	19981103	NA	Method for construction of normalized cDNA libraries	435/6
74	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5812252 A	19980922	51	Fingerprint--Acquisition apparatus for access control; personal weapon and other systems controlled thereby	356/71
75	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5811235 A	19980922	NA	Method of characterisation	435/6

	U	1	Document ID	Issue Date	Pages	Title	Curr nt OR
76	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5787279 A	19980728	NA	System and method for conformationally-flexible molecular recognition	707/6
77	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5787010 A	19980728	30	Enhanced dynamic programming method for technology mapping of combinational logic circuits	716/7
78	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5752019 A	19980512	NA	System and method for conformationally-flexible molecular identification	707/3
79	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5665582 A	19970909	NA	Isolation of biological materials	435/181
80	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5657223 A	19970812	38	Method for seismic data processing using depth slice decomposition	705/400
81	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4412288 A	19831025	NA	Experiment-machine	600/342

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030079195 A1	20030424	35	Methods and apparatuses for designing integrated circuits	716/8
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20020194572 A1	20021219	38	Methods and apparatuses for designing integrated circuits	716/1
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020157063 A1	20021024	1889	Implicit mapping of technology independent network to library cells	716/1
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20020133788 A1	20020919	29	Structured algorithmic programming language approach to system design	716/3
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020059553 A1	20020516	27	Creating optimized physical implementations from high-level descriptions of electronic design using placement-based information	716/4
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20010013113 A1	20010809	24	Technology mapping method and storage medium	716/3
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6578176 B1	20030610	21	Method and system for genetic algorithm based power optimization for integrated circuit designs	716/2
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6546539 B1	20030408	13	Netlist resynthesis program using structure co-factoring	716/12
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6543036 B1	20030401	34	Non-linear, gain-based modeling of circuit delay for an electronic design automation system	716/6
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6539536 B1	20030325	69	Electronic design automation system and methods utilizing groups of multiple cells having loop-back connections for modeling port electrical characteristics	716/18
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6519754 B1	20030211	34	Methods and apparatuses for designing integrated circuits	716/18
12	<input type="checkbox"/>	<input type="checkbox"/>	US 6519609 B1	20030211	12	Method and system for matching boolean signatures	707/104.1

	U	1	Document ID	Issue Date	Pages	Title	Current OR
13	<input type="checkbox"/>	<input type="checkbox"/>	US 6505339 B1	20030107	35	Behavioral synthesis links to logic synthesis	716/18
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6496972 B1	20021217	56	Method and system for circuit design top level and block optimization	716/18
15	<input type="checkbox"/>	<input type="checkbox"/>	US 6490717 B1	20021203	32	Generation of sub-netlists for use in incremental compilation	716/18
16	<input type="checkbox"/>	<input type="checkbox"/>	US 6484292 B1	20021119	10	Incremental logic synthesis system for revisions of logic circuit designs	716/2
17	<input type="checkbox"/>	<input type="checkbox"/>	US 6470482 B1	20021022	42	METHOD AND SYSTEM FOR CREATING, DERIVING AND VALIDATING STRUCTURAL DESCRIPTION OF ELECTRONIC SYSTEM FROM HIGHER LEVEL, BEHAVIOR-ORIENTED DESCRIPTION, INCLUDING INTERACTIVE SCHEMATIC DESIGN AND SIMULATION	716/6
18	<input type="checkbox"/>	<input type="checkbox"/>	US 6467068 B1	20021015	48	Construction of a technology library for use in an electronic design automation system that converts the technology library into non-linear, gain-based models for estimating circuit delay	716/6
19	<input type="checkbox"/>	<input type="checkbox"/>	US 6460166 B1	20021001	17	System and method for restructuring of logic circuitry	716/2
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6446240 B1	20020903	46	Evaluation of a technology library for use in an electronic design automation system that converts the technology library into non-linear, gain-based models for estimating circuit delay	716/2
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6438735 B1	20020820	36	Methods and apparatuses for designing integrated circuits	716/7

	U	1	Document ID	Issue Date	Pages	Title	Current OR
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6421818 B1	20020716	80	Efficient top-down characterization method	716/18
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6421808 B1	20020716	50	Hardware design language for the design of integrated circuits	716/1
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6397341 B1	20020528	21	Method for improving the speed of behavioral synthesis links to logic synthesis	713/400
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6378123 B1	20020423	81	Method of handling macro components in circuit design synthesis	716/18
26	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6360356 B1	20020319	31	Creating optimized physical implementations from high-level descriptions of electronic design using placement-based information	716/18
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6324679 B1	20011127	34	Register transfer level power optimization with emphasis on glitch analysis and reduction	716/18
28	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6324678 B1	20011127	45	Method and system for creating and validating low level description of electronic design	716/18
29	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6321369 B1	20011120	24	Interface for compiling project variations in electronic design environments	716/11
30	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6317860 B1	20011113	19	Electronic design automation tool for display of design profile	716/5
31	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6311317 B1	20011030	26	Pre-synthesis test point insertion	716/18
32	<input type="checkbox"/>	<input type="checkbox"/>	US 6298472 B1	20011002	33	Behavioral silicon construct architecture and mapping	716/18
33	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6298319 B1	20011002	36	Incremental compilation of electronic design for work group	703/26
34	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6295636 B1	20010925	82	RTL analysis for improved logic synthesis	716/18
35	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6292931 B1	20010918	81	RTL analysis tool	716/18

	U	1	Document ID	Issue Date	Pages	Title	Current OR
36	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6289498 B1	20010911	81	VDHL/Verilog expertise and gate synthesis automation system	716/18
37	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6289491 B1	20010911	80	Netlist analysis tool by degree of conformity	716/5
38	<input type="checkbox"/>	<input type="checkbox"/>	US 6286128 B1	20010904	11	Method for design optimization using logical and physical information	716/18
39	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6282695 B1	20010828	17	System and method for restructuring of logic circuitry	716/18
40	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6263483 B1	20010717	81	Method of accessing the generic netlist created by synopsys design compiler	716/18
41	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6237127 B1	20010522	40	Static timing analysis of digital electronic circuits using non-default constraints known as exceptions	716/6
42	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6216252 B1	20010410	53	Method and system for creating, validating, and scaling structural description of electronic device	716/1
43	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6205572 B1	20010320	79	Buffering tree analysis in mapped design	716/5
44	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6173435 B1	20010109	80	Internal clock handling in synthesis script	716/18
45	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6145117 A	20001107	31	Creating optimized physical implementations from high-level descriptions of electronic design using placement based information	716/18
46	<input type="checkbox"/>	<input type="checkbox"/>	US 6134705 A	20001017	32	Generation of sub-netlists for use in incremental compilation	716/18
47	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6080201 A	20000627	15	Integrated placement and synthesis for timing closure of microprocessors	703/14

	U	1	Document ID	Issue Date	Pages	Title	Current OR
48	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6044211 A	20000328	24	Method for graphically representing a digital device as a behavioral description with data and control flow elements, and for converting the behavioral description to a structural description	716/18
49	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6028988 A	20000222	28	System for logic synthesis-for-testability capable of improving testability for an FSM having an asynchronous reset state	716/4
50	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6026226 A	20000215	23	Local compilation in context within a design hierarchy	716/12
51	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6023566 A	20000208	18	Cluster matching for circuit implementation	716/2
52	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5991524 A	19991123	18	Cluster determination for circuit implementation	716/18
53	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5933356 A	19990803	43	Method and system for creating and verifying structural logic model of electronic design from behavioral description, including generation of logic and timing models	703/15
54	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5910897 A	19990608	32	Specification and design of complex digital systems	716/19
55	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5903466 A	19990511	37	Constraint driven insertion of scan logic for implementing design for test within an integrated circuit design	716/18
56	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5883814 A	19990316	20	System-on-chip layout compilation	716/2
57	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5880971 A	19990309	26	Methodology for deriving executable low-level structural descriptions and valid physical implementations of circuits and systems from semantic specifications and descriptions thereof	716/6

	U	1	Document ID	Issue Date	Pages	Title	Current OR
58	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5870308 A	19990209	49	Method and system for creating and validating low-level description of electronic design	716/18
59	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5867399 A	19990202	64	System and method for creating and validating structural description of electronic system from higher-level and behavior-oriented description	716/18
60	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5831868 A	19981103	35	Test ready compiler for design for test synthesis	716/18
61	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5801958 A	19980901	95	Method and system for creating and validating low level description of electronic design from higher level, behavior-oriented description, including interactive system for hierarchical display of control and dataflow information	716/18
62	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5787010 A	19980728	30	Enhanced dynamic programming method for technology mapping of combinational logic circuits	716/7
63	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5764951 A	19980609	39	Methods for automatically pipelining loops	716/1
64	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5703789 A	19971230	36	Test ready compiler for design for test synthesis	716/4
65	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5696771 A	19971209	46	Method and apparatus for performing partial unscan and near full scan within design for test applications	714/726
66	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5650938 A	19970722	19	Method and apparatus for verifying asynchronous circuits using static timing analysis and dynamic functional simulation	716/6

	U	1	Document ID	Issue Date	Pages	Title	Current OR
67	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5625567 A	19970429	28	Electronic circuit design system and method with programmable addition and manipulation of logic elements surrounding terminals	716/3
68	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5623418 A	19970422	66	System and method for creating and validating structural description of electronic system	716/1
69	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5598344 A	19970128	51	Method and system for creating, validating, and scaling structural description of electronic device	716/18
70	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5594657 A	19970114	41	System for synthesizing field programmable gate array implementations from high level circuit descriptions	716/16
71	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5572437 A	19961105	44	Method and system for creating and verifying structural logic model of electronic design from behavioral description, including generation of logic and timing models	716/18
72	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5572436 A	19961105	48	Method and system for creating and validating low level description of electronic design	716/18
73	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5557531 A	19960917	47	Method and system for creating and validating low level structural description of electronic design from higher level, behavior-oriented description, including estimating power dissipation of physical implementation	716/1

	U	1	Document ID	Issue Date	Pages	Title	Current OR
74	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5555201 A	19960910	95	Method and system for creating and validating low level description of electronic design from higher level, behavior-oriented description, including interactive system for hierarchical display of control and dataflow information	716/1
75	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5553002 A	19960903	47	Method and system for creating and validating low level description of electronic design from higher level, behavior-oriented description, using milestone matrix incorporated into user-interface	716/11
76	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5544067 A	19960806	43	Method and system for creating, deriving and validating structural description of electronic system from higher level, behavior-oriented description, including interactive schematic design and simulation	703/14
77	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5544066 A	19960806	47	Method and system for creating and validating low level description of electronic design from higher level, behavior-oriented description, including estimation and comparison of low-level design constraints	716/18
78	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5541850 A	19960730	26	Method and apparatus for forming an integrated circuit including a memory structure	716/18

	U	1	Document ID	Issue Date	Pages	Title	Current OR
79	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5541849 A	19960730	46	Method and system for creating and validating low level description of electronic design from higher level, behavior-oriented description, including estimation and comparison of timing parameters	716/18
80	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5537330 A	19960716	17	Method for mapping in logic synthesis by logic classification	716/18
81	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5526277 A	19960611	31	ECAD system for deriving executable low-level structural descriptions and valid physical implementations of circuits and systems from high-level semantic descriptions thereof	716/3
82	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5493508 A	19960220	31	Specification and design of complex digital systems	716/5
83	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5475605 A	19951212	15	Timing analysis for logic optimization using target library delay values	716/6
84	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5452227 A	19950919	11	Method and apparatus for converting a programmable logic device designed into a selectable target gate array design	716/3
85	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5222030 A	19930622	528	Methodology for deriving executable low-level structural descriptions and valid physical implementations of circuits and systems from high-level semantic specifications and descriptions thereof	716/11
86	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4703435 A	19871027	16	Logic Synthesizer	716/18

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030092133 A1	20030515	87	Interleukins-21 and 22	435/69.52
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030082537 A1	20030501	96	Methods for genetic analysis of DNA to detect sequence variances	435/6
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030073101 A1	20030417	92	Restriction enzyme genotyping	435/6
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030027255 A1	20030206	63	Stem cell maintenance factor materials and methods	435/69.1
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030003545 A1	20030102	73	INTERLEUKINS-21 AND 22	435/69.5
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020173005 A1	20021121	64	Methods and materials relating to novel CD39-like polypeptides	435/69.1
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020157063 A1	20021024	1889	Implicit mapping of technology independent network to library cells	716/1
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020156009 A1	20021024	111	Novel interleukin - 1 Hy2 materials and methods	514/12
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020150901 A1	20021017	22	Novel nucleic acids, polypeptides, methods of making, and uses thereof	435/6
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020146772 A1	20021010	45	Methods and materials relating to novel CD39-like polypeptides	435/69.1
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020142953 A1	20021003	164	Materials and methods relating to lipid metabolism	514/12
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020102567 A1	20020801	81	Method for comparing nucleic acid sequences	435/6

	U	1	Document ID	Issue Date	Pages	Title	Curr nt OR
13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020081625 A1	20020627	45	MEMBER OF THE IMMUNOGLOBULIN SUPERFAMILY AND USES THEREOF	435/7.1
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020058018 A1	20020516	58	Novel interleukin-3 and uses thereof	424/85.2
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020035742 A1	20020321	10	Powdery mildew resistant plants	800/323
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20010044431 A1	20011122	41	Prevention of ovarian cancer by administration of products that induce biologic effects in the ovarian epithelium	514/179
17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20010023070 A1	20010920	87	Interleukins-21 and 22	435/69.5
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6551784 B2	20030422	84	Method of comparing nucleic acid sequences	435/6
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6544783 B1	20030408	18	Polynucleotide sequences from rice	435/320.1
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6541623 B1	20030401	81	Interleukin--1 receptor antagonist and uses thereof	536/24.3
21	<input type="checkbox"/>	<input type="checkbox"/>	US 6539536 B1	20030325	69	Electronic design automation system and methods utilizing groups of multiple cells having loop-back connections for modeling port electrical characteristics	716/18
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6526352 B1	20030225	65	Method and arrangement for mapping a road	701/213

	U	1	Document ID	Issue Date	Pages	Title	Current OR
23	<input type="checkbox"/>	<input type="checkbox"/>	US 6496972 B1	20021217	56	Method and system for circuit design top level and block optimization	716/18
24	<input type="checkbox"/>	<input type="checkbox"/>	US 6490717 B1	20021203	32	Generation of sub-netlists for use in incremental compilation	716/18
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6485920 B1	20021126	39	Growth factor antagonist materials and methods	435/7.21
26	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6476211 B1	20021105	88	Methods and materials relating to CD39-like polypeptides	536/23.5
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6475754 B1	20021105	28	Polynucleotides encoding Bordatella bronchiseptica fimbrial proteins (FimN), vectors, and expression systems therefor	435/69.1
28	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6475736 B1	20021105	81	Methods for genetic analysis of DNA using biased amplification of polymorphic sites	435/6
29	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6472173 B1	20021029	55	Chemokine receptor obtained from a cDNA library of fetal liver-spleen	435/69.1
30	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6447771 B1	20020910	65	Methods and materials relating to novel CD39-like polypeptides	424/94.61
31	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6426191 B1	20020730	60	Assays involving an IL-1 receptor antagonist	435/7.8
32	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6423682 B1	20020723	39	Sprouty related growth factor antagonist (FGFAn-Hy) materials and methods	514/2

	U	1	Document ID	Issue Date	Pages	Title	Current OR
33	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6403320 B1	20020611	99	Support bound probes and methods of analysis using the same	435/6
34	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6387645 B1	20020514	46	Methods and materials relating to novel CD39-like polypeptides	435/21
35	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6372892 B1	20020416	52	Interleukin--1 Hy2 materials and methods	530/389.2
36	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6365726 B1	20020402	62	Polynucleotides encoding IL-1 Hy2 polypeptides	536/23.52
37	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6339141 B1	20020115	42	Interleukin-1 Hy2 materials and methods	530/351
38	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6337072 B1	20020108	60	Interleukin-1 receptor antagonist and recombinant production thereof	424/198.1
39	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6335013 B1	20020101	NA	Methods and materials relating to CD39-like polypeptides	424/94.61
40	<input type="checkbox"/>	<input type="checkbox"/>	US 6321158 B1	20011120	92	Integrated routing/mapping information	701/201
41	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6309822 B1	20011030	82	Method for comparing copy number of nucleic acid sequences	435/6

	U	1	Document ID	Issue Date	Pages	Title	Current OR
42	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6294655 B1	20010925	70	Anti-interleukin-1 receptor antagonist antibodies and uses thereof	530/388.23
43	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6134705 A	20001017	32	Generation of sub-netlists for use in incremental compilation	716/18
44	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6067650 A	20000523	46	Method and apparatus for performing partial unscan and near full scan within design for test applications	714/726
45	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6030791 A	20000229	19	Antibody for a homolog of rat elastase IV derived from human pancreas	435/7.1
46	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5948040 A	19990907	60	Travel reservation information and planning system	701/201
47	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5937190 A	19990810	95	Architecture and methods for a hardware description language source level analysis and debugging system	717/131
48	<input type="checkbox"/>	<input type="checkbox"/>	US 5903466 A	19990511	37	Constraint driven insertion of scan logic for implementing design for test within an integrated circuit design	716/18
49	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5856109 A	19990105	19	Homolog of rat elastase IV derived from human pancreas	435/7.6
50	<input type="checkbox"/>	<input type="checkbox"/>	US 5831868 A	19981103	35	Test ready compiler for design for test synthesis	716/18

	U	1	Document ID	Issue Dat	Pages	Title	Current OR
51	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5817479 A	19981006	30	Human kinase homologs	435/69.1
52	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5787010 A	19980728	30	Enhanced dynamic programming method for technology mapping of combinational logic circuits	716/7
53	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5780268 A	19980714	15	Chemokine expressed in a mixed lymphocyte reaction	435/69.5
54	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5738991 A	19980414	19	Method of detecting elastase IV	435/6
55	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5703789 A	19971230	36	Test ready compiler for design for test synthesis	716/4
56	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5696771 A	19971209	46	Method and apparatus for performing partial unscan and near full scan within design for test applications	714/726
57	<input type="checkbox"/>	<input type="checkbox"/>	US 5625567 A	19970429	28	Electronic circuit design system and method with programmable addition and manipulation of logic elements surrounding terminals	716/3
58	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5500808 A	19960319	38	Apparatus and method for estimating time delays using unmapped combinational logic networks	703/19
59	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4703435 A	19871027	16	Logic Synthesizer	716/18
60	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4412288 A	19831025	NA	Experiment-machine	600/342